

*A2
Pb.
C1*

3. (Amended) A voltage generating/transferring circuit according to claim 1, further comprising:

a third transistor which has a gate connected to the output node, and transfers a third voltage,

wherein a second voltage of the gate of said third transistor is equal to, or larger than a sum of the third voltage and a threshold voltage of said third transistor.

Kindly add claims 10-22 as follows:

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C1*

10. (New) A voltage generating/transferring circuit according to claim 1, further comprising:

a third transistor which has a gate connected to the output node, and transfers a third voltage,

wherein a second voltage of the gate of said third transistor is equal to, or larger than a sum of the third voltage and a threshold voltage of said third transistor in transferring the third voltage.

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B3
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11. (New) A voltage generating/transferring circuit comprising:

a boost unit group including a plurality of boost units series-connected between input and output nodes;

a first transistor connected between the input node and a node for receiving a first voltage; and

a first capacitor connected to the output node,

wherein each of the boost units has input and output portions, a second transistor having a gate and a drain connected to the input portion and a source connected to the output portion, and a second capacitor in each of the boost units connected to the input portion, a charge moves

between the output portion of one of the boost units and the input portion of another of the boost units, and a gate of said first transistor is connected to the input portion of one of the boost units.

12. (New) A voltage generating/transferring circuit according to claim 11, wherein said boost unit group includes not less than three boost units.

13. (New) A voltage generating/transferring circuit according to claim 11, further comprising:

a third transistor which has a gate connected to the output node, and transfers a third voltage,

wherein a second voltage of the gate of said third transistor is equal to, or larger than a sum of the third voltage and a threshold voltage of said third transistor.

14. (New) A voltage generating/transferring circuit according to claim 11, wherein a first oscillation signal is input to an even-numbered boost unit from the input node, a second oscillation signal is input to an odd-numbered boost unit from the input node, and the first and second oscillation signals have opposite phases or different timings.

15. (New) A voltage generating/transferring circuit according to claim 11, wherein gate and source voltage levels of said first transistor gradually rise while changing in opposite phases.

16. (New) A voltage generating/transferring circuit according to claim 11, further comprising:

a circuit for fixing the gate of said first transistor to low level in an OFF state.

17. (New) A voltage generating/transferring circuit according to claim 11, wherein a threshold voltage of the second transistor in at least one of the boost units is lower than a threshold voltage of said first transistor.

18. (New) A voltage generating/transferring circuit according to claim 17, wherein a transistor having a threshold voltage lower than the threshold voltage of said first transistor is arranged in a boost unit closest to the output node.

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19. (New) A voltage generating/transferring circuit according to claim 11, wherein a threshold voltage of a transistor in a boost unit on the output node side is lower than a threshold voltage of a transistor in a boost unit on the input node side.

20. (New) A voltage generating/transferring circuit according to claim 11, further comprising:

a third transistor which has a gate connected to the output node, and transfers a third voltage,

wherein a second voltage of the gate of said third transistor is equal to, or larger than a sum of the third voltage and a threshold voltage of said third transistor in transferring the third voltage.

A³

21. (New) A voltage generating/transferring circuit comprising:

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a boost unit group including at least a first boost unit and a second boost unit series-connected between input and output nodes;

a first transistor connected between the input node and a node for receiving a first voltage; and

a first capacitor connected to the output node,

wherein each of said first and second boost units has an input portion, an output portion, a second transistor having both a gate and drain connected to the input portion and a source connected to the output portion, and a second capacitor in each of said first and second boost units connected to the input portion, the source of the second transistor of said first boost unit being directly connected to the input portion of said second boost unit, and a gate of said first transistor being connected to the input portion of one of said first and second boost units.

sub C1

22. (New) A voltage generating/transferring circuit of claim 21, wherein the gate of said first transistor is directly connected to the input portion of one of said first and second boost units.